**DAWOOD UNIVERSITY OF ENGINEERING AND TECHNOLOGY**

**ASSESSMENT OF LABORATORY REPORTS/MANUALS/FOLDERS (Marks)**

Department: **Computer System Engineering,** Batch **2019,** Date of Conduct of Examinations: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

Subject: **Digital Logic Design (PR), 2nd Year**, 3rd **,** Semester, **2019 Batch**

Name of Project Examiner: **Engr. Abdul Manan Memon**

Total marks: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

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| **S. No** | **Seat No** | **Abstract/Summary**  **(3)** | **Introduction**  **(3)** | **Experimental procedure**  **(6)** | **Results: data, figures, graphs, tables, etc.**  **(3)** | **Discussion**  **(5)** | **Total marks obtained**  **3+3+6+3+5=20** |